LOC Poster Assignment:

All student teams are required to complete a digital poster as part of their project documentation.

The poster will be graded on clarity of concept (concise and clear explanation of project goals and design process), aesthetics (visually appealing, well organized, appropriate use of visuals, spelling and grammar), data analysis (well labeled graphs with proper explanation), professionalism, and proper acknowledgements.

It is recommended that you employ online tips and examples available after Google searching “how to make an academic poster.”

Things to include:

- In the Intro:
  - The purpose, rationale, and context of this project (Consider the audience—oftentimes, the audience has knowledge of engineering, but does not know anything about your specific project)
  - Explanation of what the LOC is, and its applications

- In the following sections, explain the design process in chronological order:
  - The rationale behind first chip design features
  - Initial and final sketches or drawings of the first chip—final drawing should be in SolidWorks
  - Photographs of the chip(s) in use
  - Performance graphs of first chip (calibration, etc.)
  - Differences between chip 1 and 2, and why (explain any modifications to chip 2)
  - Initial and final sketches and drawings of second chip—final drawing with dimensioning should be in Solidworks
  - Performance graphs of second chip (calibration, etc.) and rationale behind the chip chosen for final testing
  - Results and analysis of final test (i.e. what can we conclude from the results?)
  - Summary and conclusions

Poster Submission Process:

Digital posters must be completed using the provided PowerPoint template (located on the “Nano documents” website) and submitted via Carmen Dropbox by the due date listed on the class website.

When submitting your poster, the filename should be as follows:
“GTA_Nano_Poster_Team_LETTER”

Example: Smith_Nano_Poster_Team_A